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GM UNIVERSITY

P. B. Road, Davanagere – 577 006 KARNATAKA | INDIA



## Contact

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## Mr. Manjunatha Y

Assistant Professor in EEE

### Faculty

Faculty of Engineering and Technology

### School / Program

Faculty of Engineering and Technology/  
B.Tech. in Electrical and Electronics Engineering

### Faculty Introduction (Five to Six Lines)

Ms. Manjunatha Y is currently pursuing his Ph.D. at Visvesvaraya Technological University, Belagavi. He holds an M.Tech in VLSI from PES University, Bengaluru, and a B.E. in Electronics and Communication Engineering from UBDT College of Engineering, Davanagere. With over six years of combined academic and industry experience, his areas of expertise include Digital VLSI Design and Image Processing. He has guided several undergraduate projects and organized multiple workshops and training programs in embedded systems and PCB design. His current research focuses on developing deep learning approaches for early detection of kidney stones.

## Qualifications

### Ph.D. (Pursuing)

Visvesvaraya Technological University, Belagavi.

### M. Tech. (VLSI)

PES University – Bengaluru, Karnataka, 2020.

### BE (ECE)

UBDT College of Engineering, Davanagere, 2018.

## Experience

### Teaching

- 2 Years at STJP, Harapanahalli.
- 3 Years at GM Institute of Technology, Davanagere.

### Industry

- 1 Years at SION Semiconductors Private Limited, Bangalore.



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### Research

- A Novel Deep Learning Approach for Early Detection of Kidney Stone.

### Training Program Attended

- FDP on "Analog and Digital Design Flow for VLSI Chip Application" organized by AICTE Academy, GM Institute of Technology, Davangere from Feb 20 – March 03, 2025.
- Workshop on "PCB Design and Fabrication using Ki-Cad software" organized by GM Institute of Technology, Davangere from March 20-22, 2025.

### Research Interest

- Digital VLSI
- Image Processing

### Administrative Responsibilities

- Department Level: EMS Coordinator, NSS Coordinator, Alumini Coordinator, 5's Coordinator, Mentor.
- University Level: PG Coordinator.

### Workshops / FDPs / Seminars Attended

- FDP on "Analog and Digital Design Flow for VLSI Chip Application" organized by AICTE Academy, GM Institute of Technology, Davangere from Feb 20 – March 03, 2025.

### Workshops / FDPs / Seminars Organized

- Workshop on "Embedded System with 8051 Microcontrollers" organized by GM Institute of Technology, Davangere from June 22-24, 2023.
- Workshop on "PCB Design and Fabrication using Ki-Cad software" organized by GM Institute of Technology, Davangere from March 20-23, 2025.

### Projects Guided

- UG Projects - Development of Dynamo – Solar Powered E – Bicycle, Electrically Charged Green Seep.